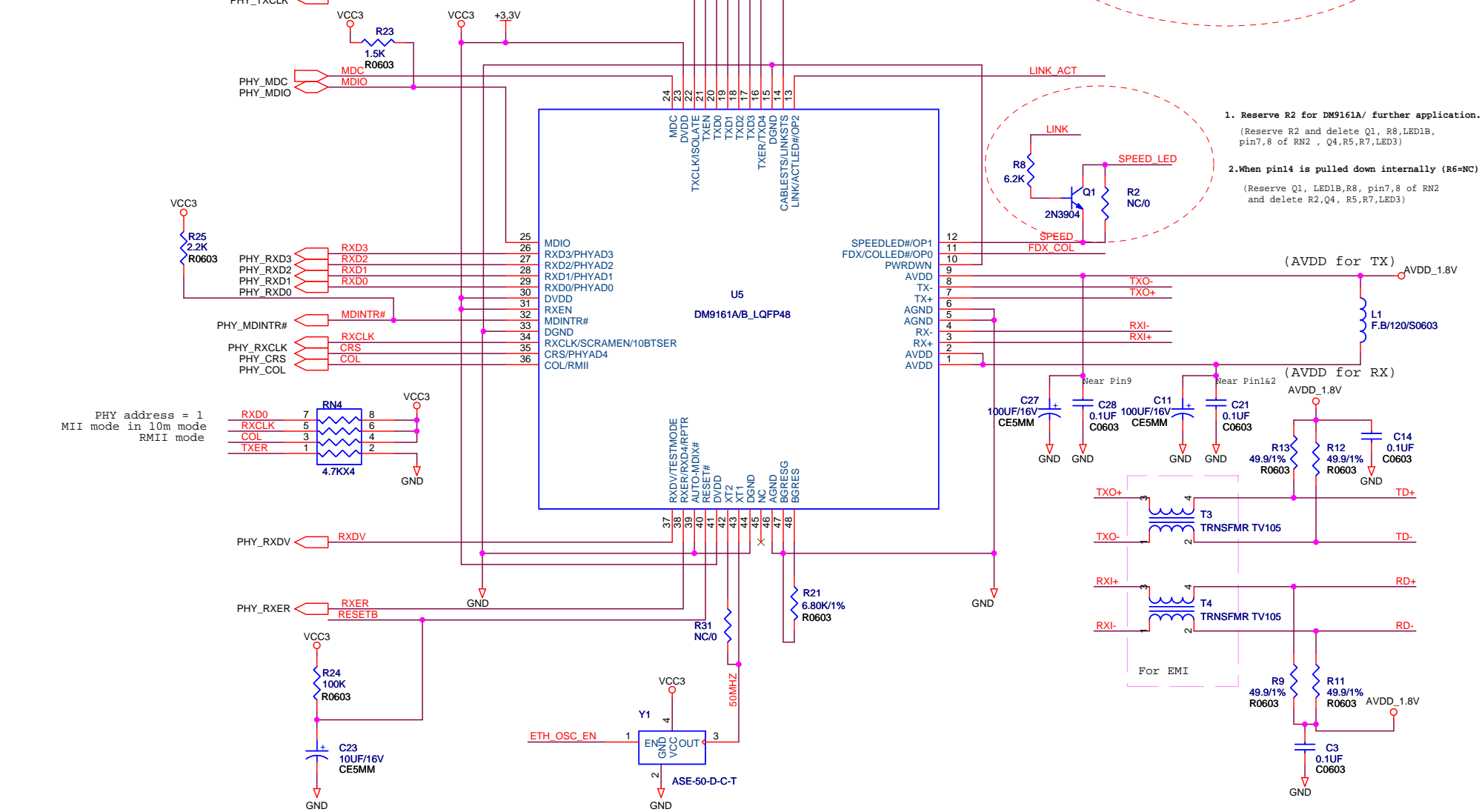
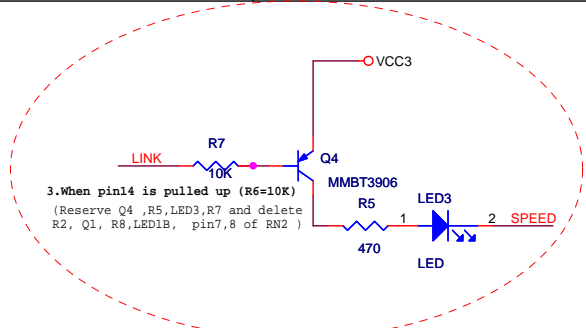


Net remap to mbed-005.1
(For replace DP83848J)

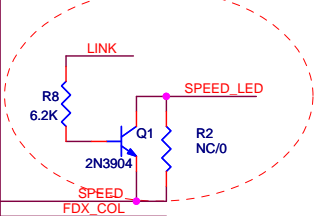
TGT P1.4	TXEN
TGT P1.0	TXD0
TGT P1.1	TXD1
TGT P1.8	RXDV
TGT P1.14	RXER
TGT P1.9	RXD0
TGT P1.10	RXD1
TGT P1.16	MDC
TGT P1.17	MDIO
ETH_RST	RESETB
LED_LINK	LINK_ACT
LED_SPEED	SPEED_LED



Note: please refer page8 and 12 of datasheet for how to choose " pull down(R6=NC) /pull up(R6=10K) "



3. When pin14 is pulled up (R6=10K)
(Reserve Q4 , R5, LED3, R7 and delete R2, Q1, R8, LED1B, pin7,8 of RN2)



1. Reserve R2 for DM9161A/ further application.
(Reserve R2 and delete Q1, R8, LED1B, pin7,8 of RN2 , Q4,R5,R7,LED3)
2. When pin14 is pulled down internally (R6=NC)
(Reserve Q1, LED1B,R8, pin7,8 of RN2 and delete R2,Q4, R5,R7,LED3)

PHY address = 1
MII mode in 10m mode
RMII mode

Title		
DM9161B with AUTO-MDIX transformer		
Size	Document Number	Rev
	DM9161B-MII-V04-M	1.5-M
Date:	Thursday, April 29, 2010	Sheet 1 of 2